

[illegible]

FIG. 2

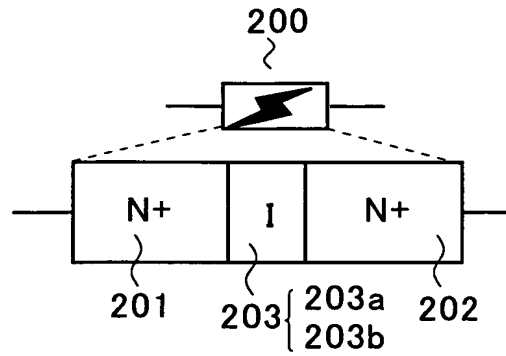


FIG. 3A

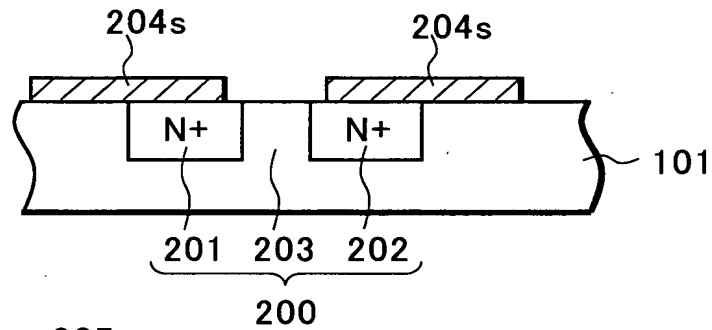


FIG. 3B

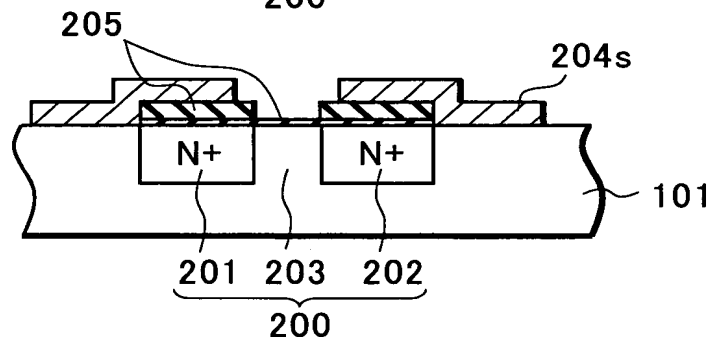


FIG. 3C

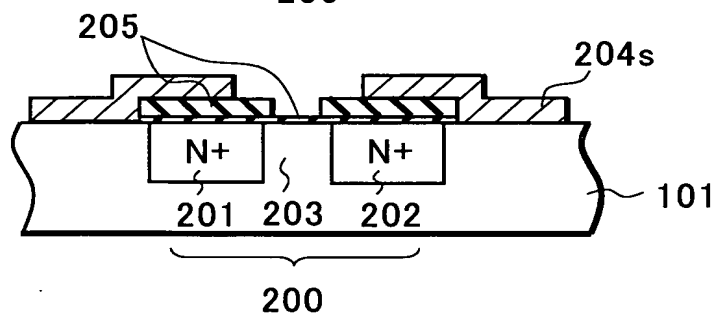


FIG. 3D

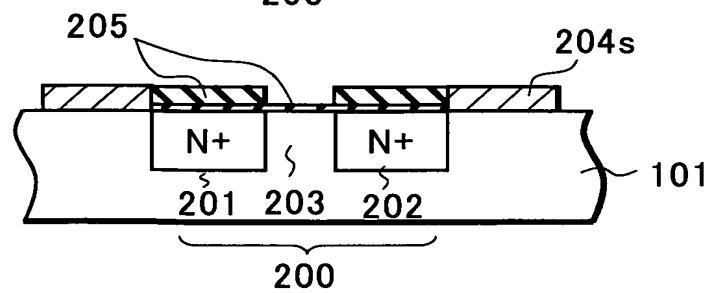


FIG.4A

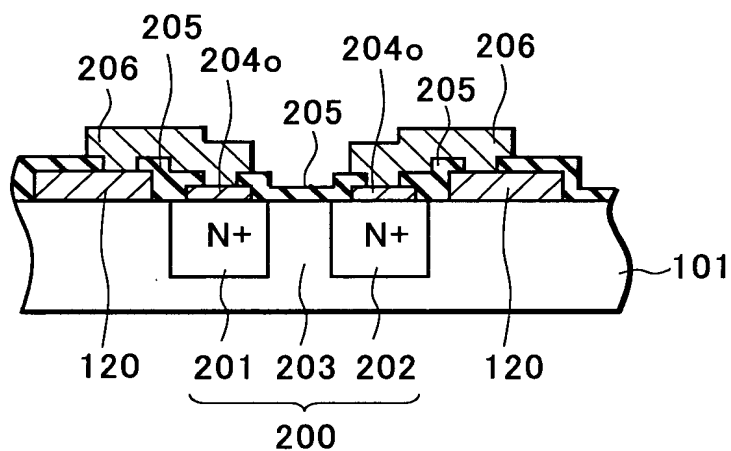


FIG.4B

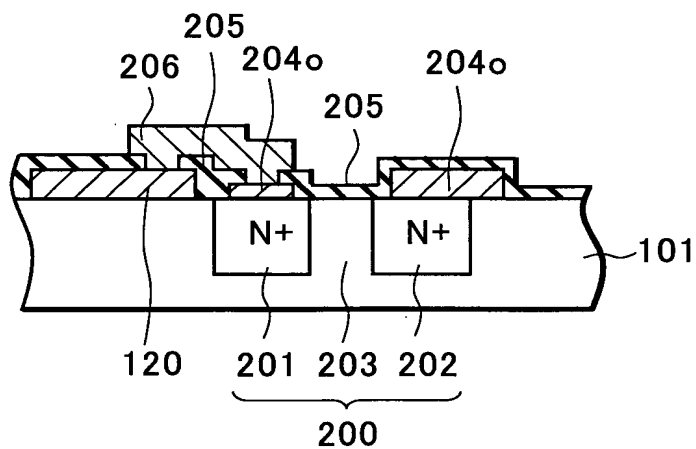


FIG. 5A

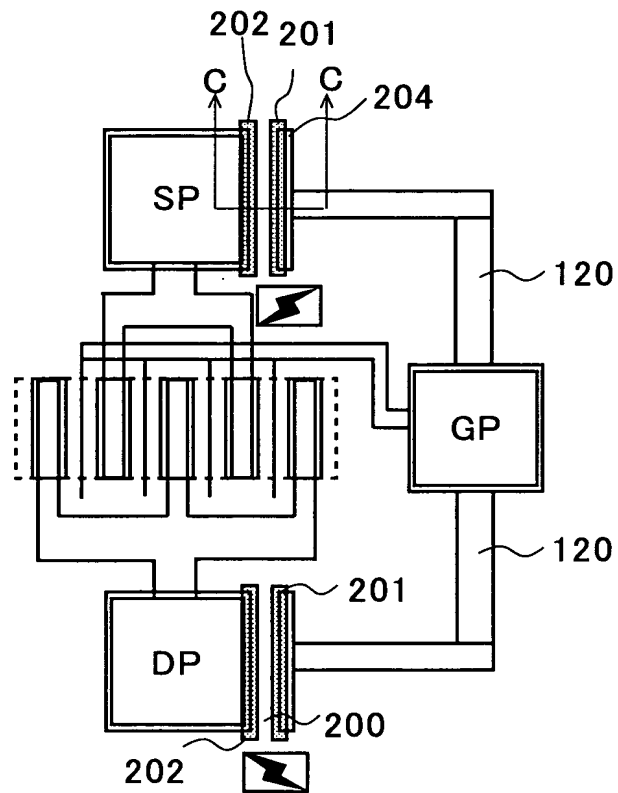


FIG. 5B

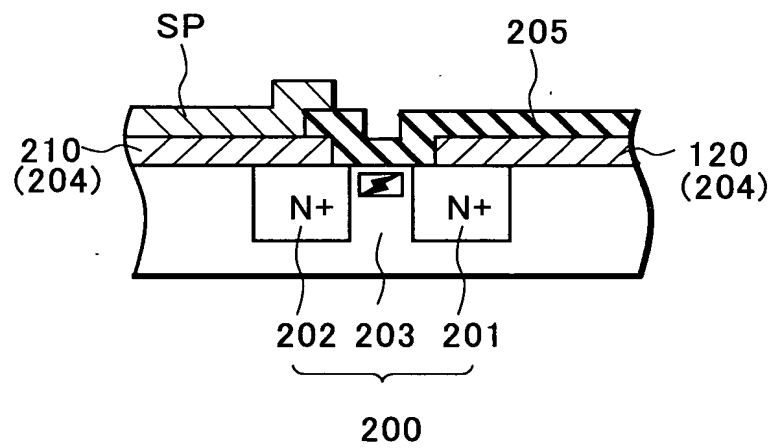


FIG. 6

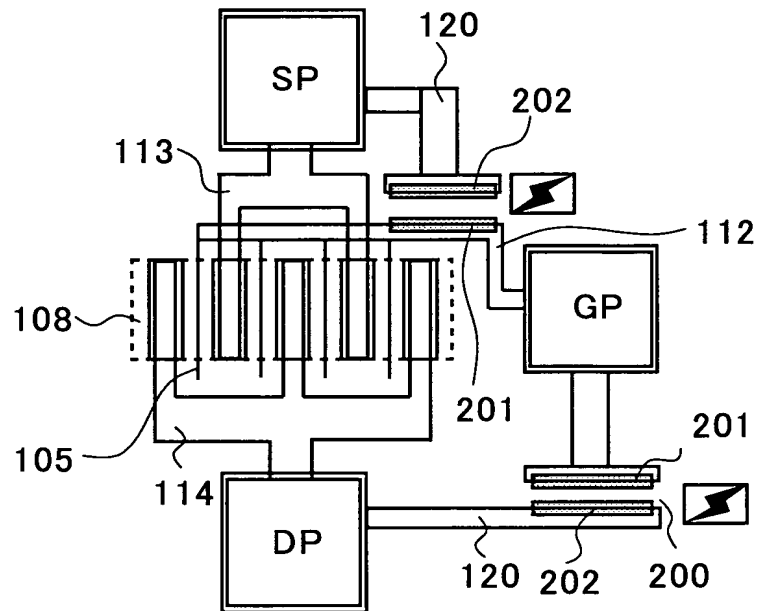


FIG. 7A

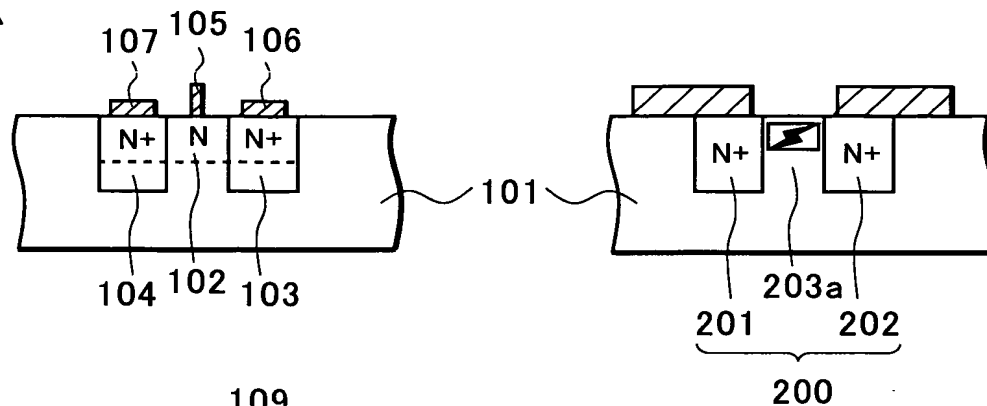


FIG. 7B

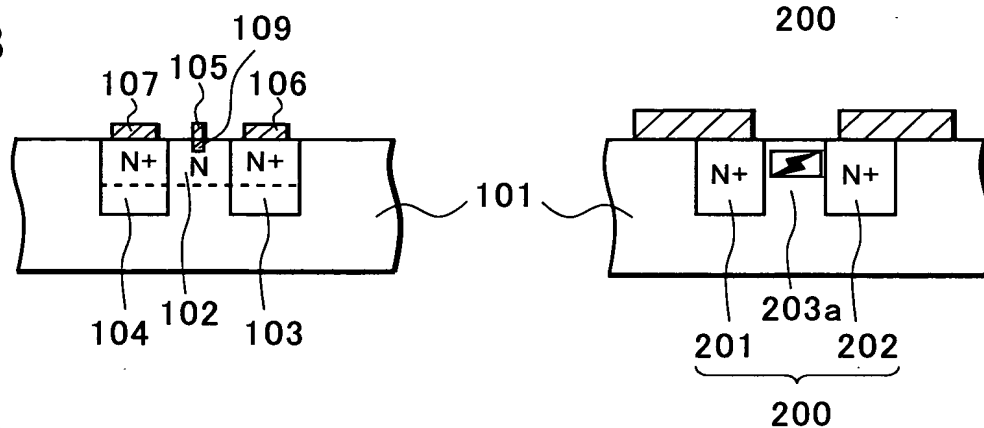


FIG. 7C

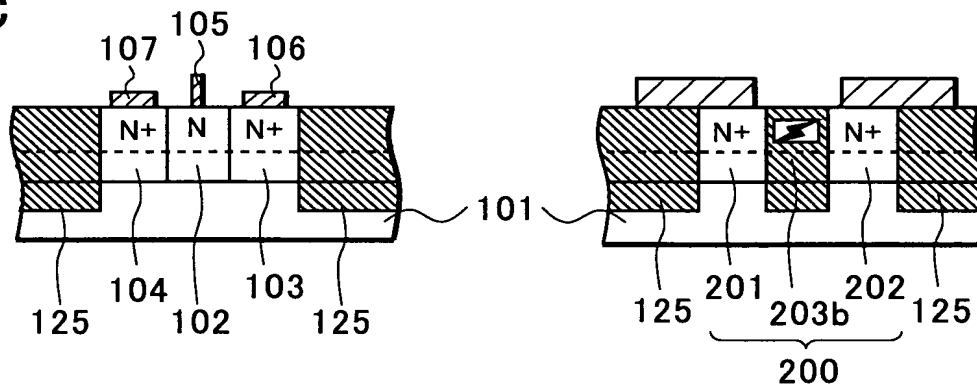


FIG. 7D

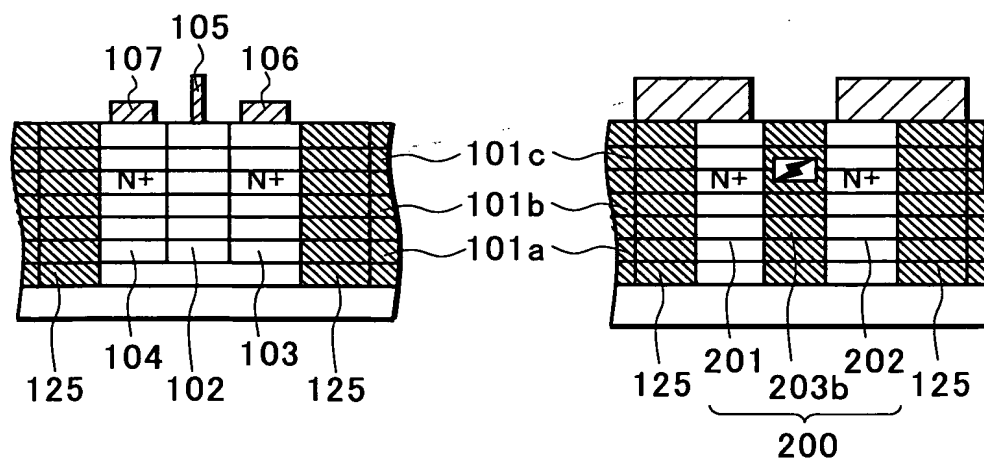


FIG.8A

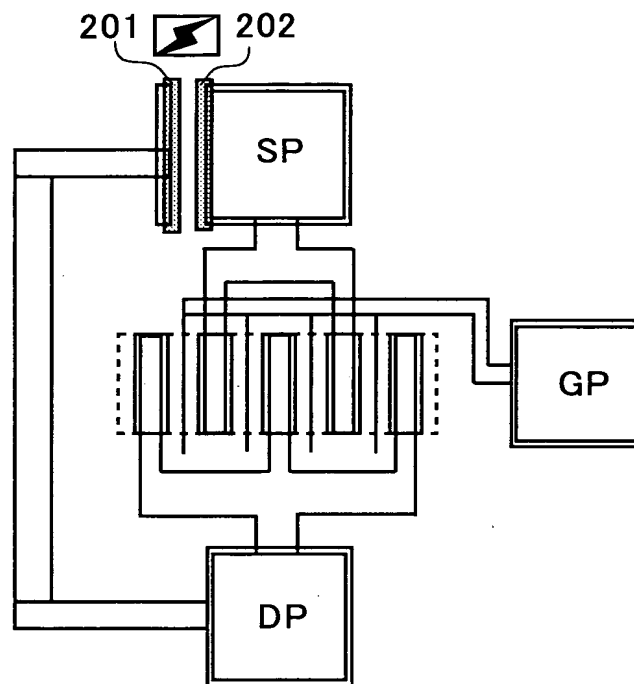


FIG.8B

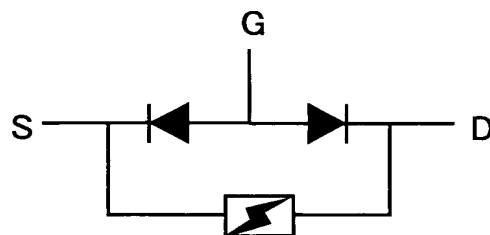


FIG.9

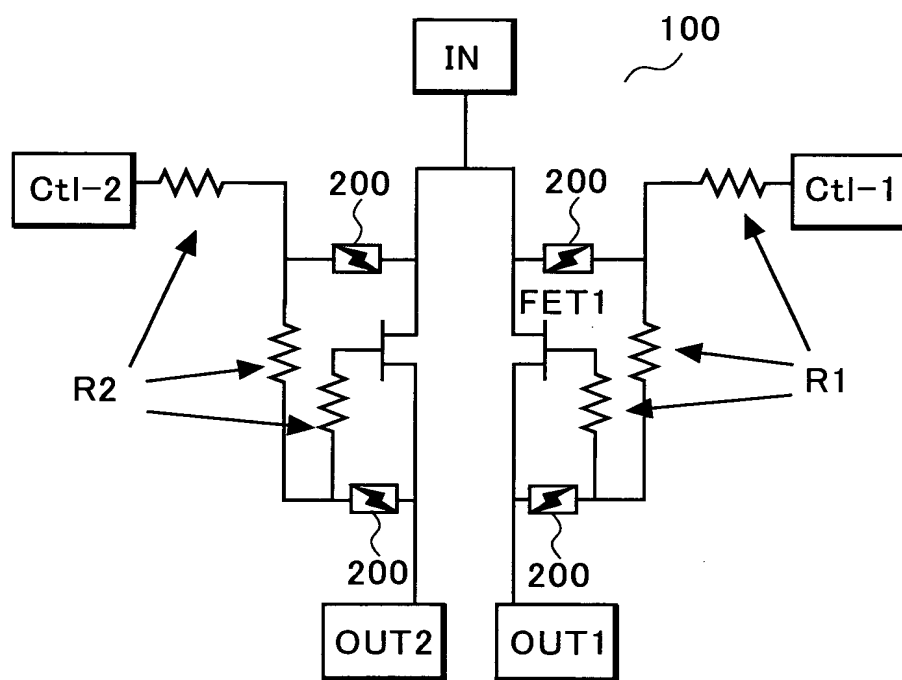


FIG.10A

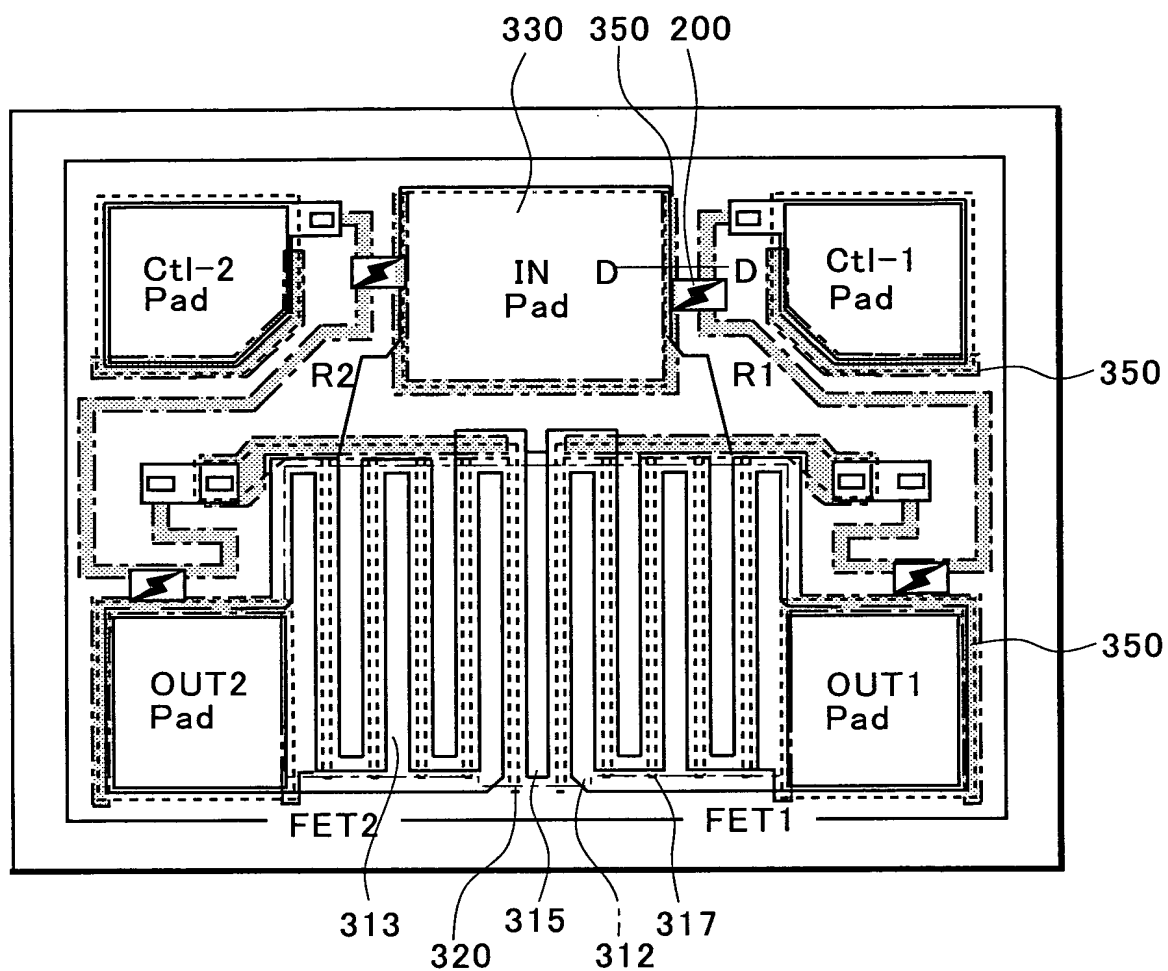


FIG. 10B

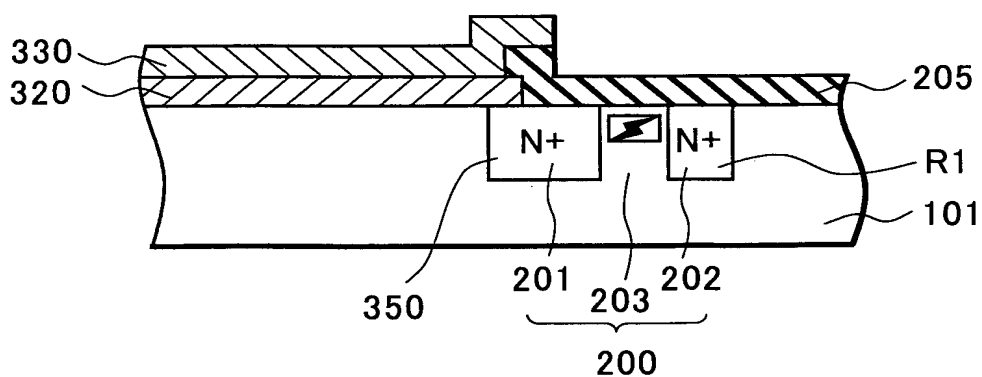


FIG. 11

Electrostatic withstand voltage
across respective terminals (unit: V)

| | |
|--------------|-----|
| IN - Ctl-1 | 700 |
| IN - Ctl-2 | 700 |
| OUT1 - Ctl-1 | 700 |
| OUT2 - Ctl-2 | 700 |

FIG. 12

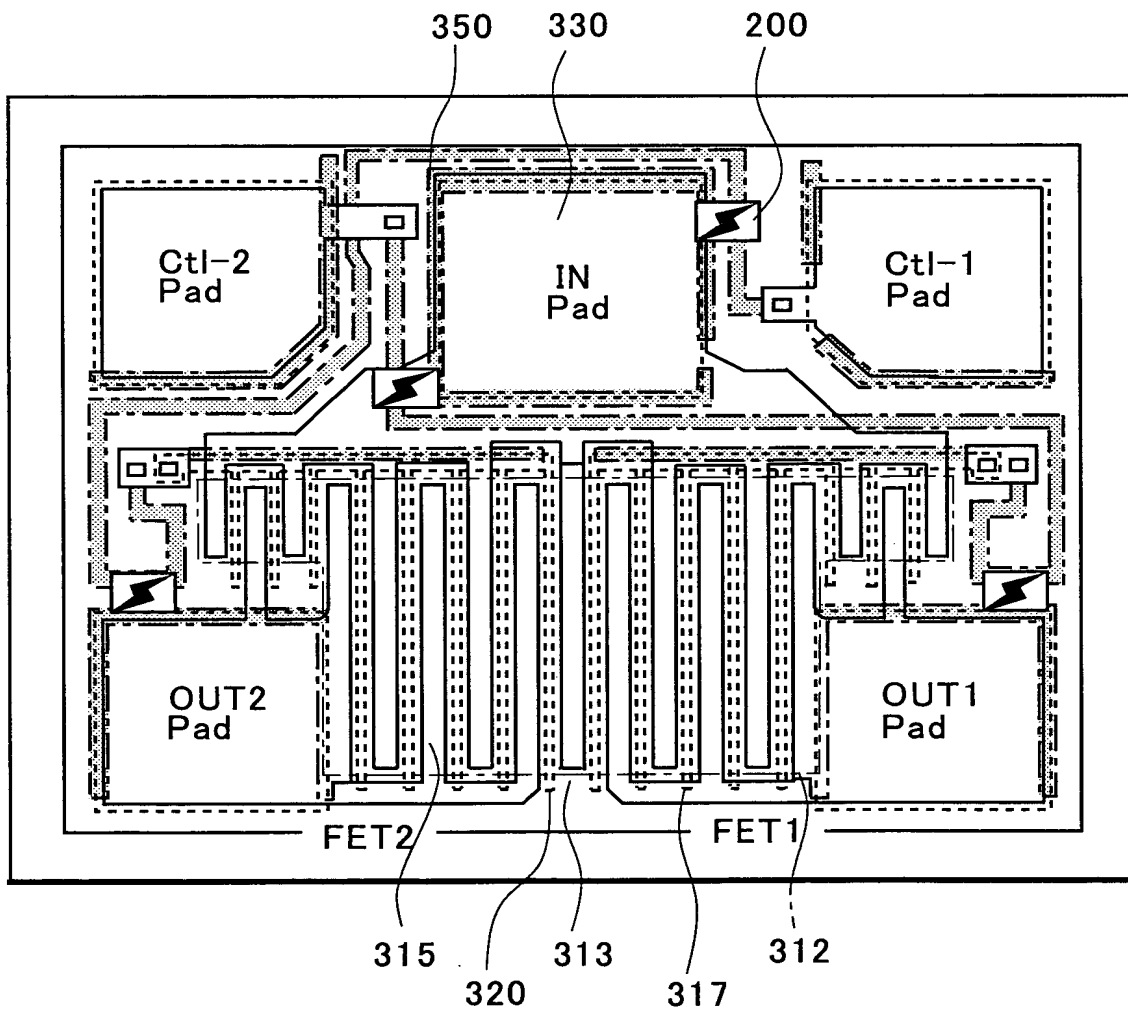


FIG.13

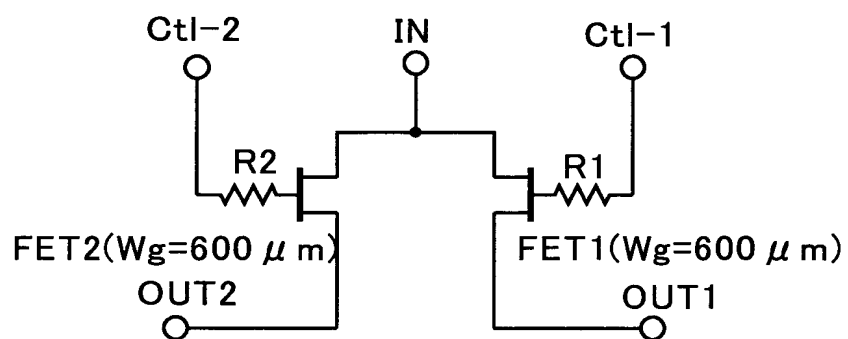


FIG.14

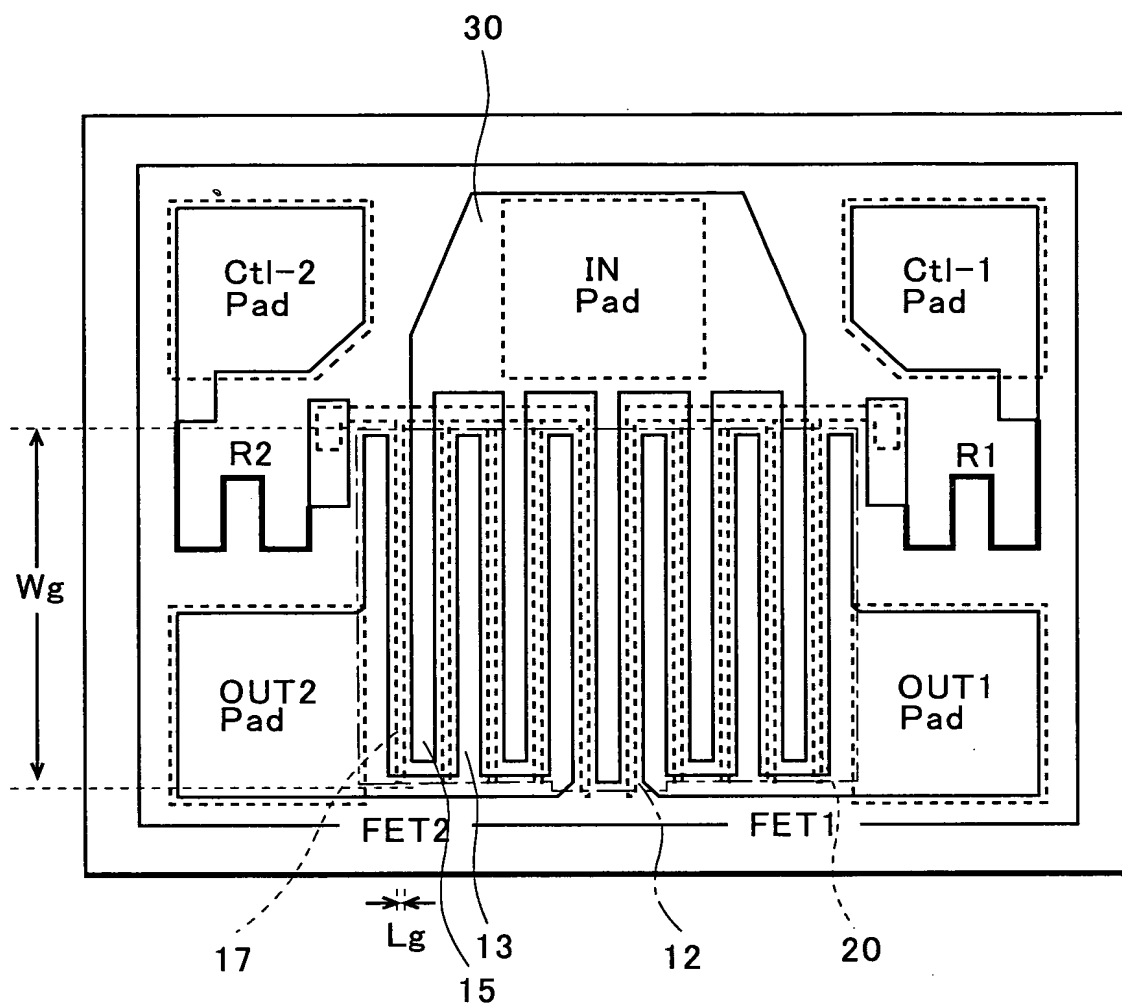


FIG.15

Electrostatic withstand voltage
across respective terminals (unit: V)

| | |
|--------------|-----|
| IN - Ctl-1 | 140 |
| IN - Ctl-2 | 140 |
| OUT1 - Ctl-1 | 500 |
| OUT2 - Ctl-2 | 450 |